L Number	Hits	Search Text	DB	Time stamp
-	18	(3,972,028 4,050.096 4,159,516 4,263,650 4,368,512 4,398,265 4,407,016 4,513,389 4.692,895 4,727,477 4,777,591 4,868,822 4,958,277 5,123,107 5,151,986 5,426,772 5,448,744 5,537,584 5,617,559 5,813,022).pn.	USPAT	2004/08/11 19:52
-	1	"894909"	US-PGPUB	2004/08/11 19:52
-	1693	"chip select" with clock	USPAT	2004/08/11 20:19
-	8	"chip select" with "clock frequency"	USPAT	2004/08/11 19:54
-	1	5822572.URPN.	USPAT	2004/08/11 20:08
-	26	13.ab.	USPAT	2004/08/11 20:11
-	33	"chip select" same (cpu processor) with frequency	USPAT	2004/08/12 10:33
-	7787	"chip select"	USPAT	2004/08/12 10:43
-	18	"chip select" with power adj sav$3	USPAT	2004/08/12 10:42
-	5	"chip select" same power adj sav$3 not ("chip select" with power adj sav$3)	USPAT	2004/08/12 10:43
-	2019	"chip select" same (cpu processor)	USPAT	2004/08/12 10:44
-	51	("chip select" same (cpu processor)) same "low power"	USPAT	2004/08/18 16:51
-	66	("chip select" same (cpu processor)) same (suspend standby)	USPAT	2004/08/12 10:49
-	847	divider with cpu	USPAT	2004/08/12 15:14
-	24	"programmable divider" with cpu	USPAT	2004/08/12 15:18
-	20	"programmabie divider" same cpu with frequency	USPAT	2004/08/12 15:19
-	97	4893271.URPN.	USPAT	2004/08/12 15:24
-	8	"569935"	USPAT	2004/08/17 15:58
-	4	"5481690"	USPAT	2004/08/17 15:58
-	7801	"chip select"	USPAT	2004/08/17 16:47
-	51	"chip select" with duration	USPAT	2004/08/17 16:49
-	2	"chip select" with "duty cycie"	USPAT	2004/08/17 16:52
-	7	5813041.URPN.	USPAT	2004/08/17 16:52
-	0	"chip select" with percent$3	USPAT	2004/08/17 16:53
-	9	"chip select" with ratlo	USPAT	2004/08/17 16:56
-	9	("chip enable" ce) with "duty cycle"	USPAT	2004/08/17 16:57
-	1	("z-world" zworld).as.	USPAT	2004/08/18 15:32
-	2	("z-worid" zworld).as.	US-PGPUB	2004/08/18 15:33
-	0	"rabbit semiconductor".as.	US-PGPUB	2004/08/18 15:34
-	0	"rabbit semiconductor".as.	USPAT	2004/08/18 15:34
-	0	darlrymple-monte-$.in.	USPAT	2004/08/18 15:34
-	0	darlrymple-monte-$.in.	US-PGPUB	2004/08/18 15:34
-	0	darlrymple-monte-$.in.	EPO; JPO; DERWENT	2004/08/18 16:13
-	1	short$5 adj "chip select"	EPO; JPO; DERWENT	2004/08/18 16:16
-	3	short$5 adj "chip select"	USPAT	2004/08/18 16:18
-	2	short$5 adj "chip select"	US-PGPUB; IBM_TDB	2004/08/18 16:17
-	2	reduc$4 adj "chip select"	USPAT	2004/08/18 16:18

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-		4	reduc$4 adj "chip select"	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/18 16:19
-		5	"chip select" same "duty cycle"	US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/18 16:22
-		112	programmable adj "chip select"	USPAT	2004/08/18 16:23
-		341	doubler same divider	USPAT	2004/08/18 16:51
-		178	doubler with divider	USPAT	2004/08/18 16:51
-		2	(doubler with divider) and 713/$.ccls.	USPAT	2004/08/18 16:52